

DSG-NPS R&D Meeting

Date: September 29, 2020

Time: 11:00 – 12:00

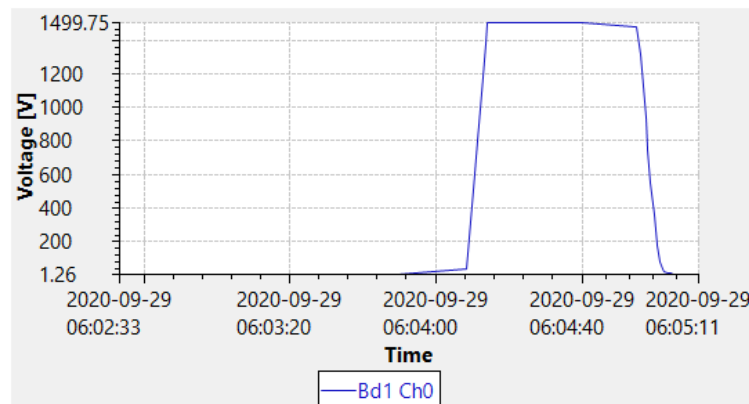
Attendees: Mary Ann Antonioli, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Tyler Lemon, Marc McMullen, Amrit Yegneswaran

1. CSS screen development status

- 1.1. Voltage and current limit settings screens for each PMT—180 of 1080 screens completed
- 1.2. In response to an email about setting module temperature alarm thresholds, Brad Sawatzky suggested that the module temperature PVs be added to the Hall C alarm handler.
 - 1.2.1. DSG will provide the PVs to be added to the alarm handler, but will also continue development of the interlock system

2. CAEN testing and data analysis

- 2.1. George will be completing all testing data analysis using Excel; fourteen modules remain to be analyzed for current stability
- 2.2. Software for over current trip testing has been completed
- 2.3. Software for over voltage trip testing in progress
- 2.4. Discussion about time zone issue for trip test plots
 - 2.4.1. Time appears to be set to UTC (4 hours behind EST) and is not converting to EST
 - 2.4.2. Best option may be to use data logger and plot test data using Python plotting package



Screenshot of trip test CSS-BOY plot.

- 2.5. Mary Ann will be running the Python package for trip test data analysis

3. HV divider cables fabricated—820 of 1100

4. Amrit inquired about timeline for testing the HV divider cables being made by DSG here at JLab – Aaron will reach out to Stephen Wood to find out if this is being planned.